

**SEVEN DAYS FDTP
ON
EE6301 - DIGITAL LOGIC CIRCUITS
10th – 16th May, 2017**

REGISTRATION FORM

Coordinators

Mrs.R.DEEPA
Assistant Professor (Senior Grade)/EIE

Dr.S.P.SELVARAJ
Associate Professor/EIE



Approved by

**Anna University
Chennai**

Organized by



Department of Electronics and Instrumentation
Engineering
Bannari Amman Institute of Technology
(An Autonomous Institution)
Sathyamangalam – 638 401.

Book-Post

**ANNA UNIVERSITY, CHENNAI Sponsored Faculty Development
and Training Programme on EE 6301 – Digital Logic Circuits**

Address for Correspondence

Mrs . R.Deepa

Coordinator – FDTP

AP(Sr.G)/ EIE

Bannari Amman Institute of Technology,

Sathyamangalam – 638 401

Ph: 9994059416, 9842293032

ORGANIZING COMMITTEE

CHIEF PATRON : Dr.M.Rajaram
Vice-Chancellor,
Anna University, Chennai.

PATRON : Dr.S.Ganesan
Registrar,
Anna University, Chennai.

CHAIR : Dr.K.Shanthi
Director, CFD
Dr.D.Sridharan
Addl Director, CFD

CO-CHAIR : Dr.D.Saravanan
Principal
Bannari Amman Institute
of Technology

Dr.D.Sharmila
HOD/EIE

Co-ordinators : Mrs. R.Deepa
AP (Sr.G)/ EIE
Dr. S. P. Selvaraj
Associate Professor/EIE

Venue : Smart Class Room
Department of EIE

IMPORTANT DATES

Submission of Application : 05.05.17

Intimation of Selection : 06.05.17

Confirmation by Participants : 07.05.17

ABOUT THE INSTITUTION

The Bannari Amman Institute of Technology is the fruit of dedicated works done by the Bannari Amman Group, a leading corporate house in South India, to establish a Centre of Excellence in Engineering and Technology. It is an AICTE approved, ISO 9001:2008 certified, Autonomous Institution affiliated to Anna University, Chennai and accredited by NBA and NAAC with „A“ grade. BIT offers 15 UG and 15 PG programmes in Engineering and Technology, MBA and PhD/M.S. The Institute has also been bestowed with “Bharathiya Vidya Bhavan Best Engineering College” award by ISTE, New Delhi in 2009.

ABOUT THE DEPARTMENT

The Department of Electronics and Instrumentation Engineering has been established in the year 2007 and has been offering a 4 year bachelor programme and 2 year post graduate programme in Instrumentation Engineering and Embedded Systems. In spite of being a late bloomer, it has become one of the pioneer departments in the college and has attracted numerous students from all parts of India. Our department has achieved 100% placement for the past six consecutive years. The department has a dedicated team of 31 faculty members who are highly qualified and have a wealth of experience. The department has well equipped laboratories to enhance the technical expertise of the students. The Process Control laboratory and Centre of Excellence for Industrial Automation is the highlight of the department.

The computer centre comprises of 80 systems with all latest software and internet facilities that aid the students to perform better in their logical aspects.

Eligibility & Selection

Teaching Faculty from Affiliated Engineering Colleges of Anna University is eligible to attend the programme. Number of Participants is limited to 25. Priority will be given for young faculty members. The Participants will be selected in First Come First Serve basis.

Registration Details & Intimation

No Registration fee. All the duly filled and signed registration forms should be sent to the Coordinator by post or courier. A Scanned Copy of the filled application may be emailed to the coordinator. (deepar@bitsathy.ac.in) Selection Intimation through email only.

Accommodation

Based on the request through mail/phone, Accommodation will be given in the college hostel at nominal Cost.

Address for Communication

Mrs. R.DEEPA

Coordinator – FDTP

Assistant Professor (Sr.G), Department of EIE,
Bannari Amman Institute of Technology,
Sathyamangalam – 638 401.

E-Mail: deepar@bitsathy.ac.in

Phone: 9994059416, 9842293032

EE6301 – DIGITAL LOGIC CIRCUITS

10.05.2017 to 16.05.2017

Name:

Age: Sex: Male / Female

Designation & Department:

Qualification:

Name & Address of the College:

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Phone No:

E-mail:

Is Accommodation Required? YES / NO

The information provided by me is true to the best of my knowledge. I agree to abide by the rules and Regulations governing the FDTP. If selected, I shall attend the programme for the entire duration I also undertake the responsibility to inform my absence in advance to the organizers.

(Signature of the Applicant)

Dr./Mr./Mrs.....is an employee of our Institution/ Organization and is hereby sponsored to participate in the Seven Days FDTP on Digital Logic Circuits during 10.05.2017 to 16.05.2017 at Bannari Amman Institute of Technology, Sathyamangalam – 638 401.

Place:

Date :

Signature & Seal of the Sponsoring Authority