3RD NATIONAL CONFERENCE ON ENERGY POWER INTEGRATED CIRCUITS AND SYSTEMS (EPICS-2024)

FEBRUARY 22, 2024

Registration Form

Name:	
Department:	
Designation:	
Address:	
Phone No.:	
Email:	
DD no.:	
Issuing Bank:	
Amount:	
Place:	
Date:	
Sign. of applicant	Sign. of Head of the Department

REGISTRATION

Participants are requested to register by using the registration link after the stipulated payment. The registration fee can be paid online (or) via demand draft in favor of "Principal, Bannari Amman Institute of Technology", payable at Sathyamangalam.

Name : BIT-Center for Advanced Studies

A/C Number : 911010036862582 Account Type : Savings Account

Branch : Axis Bank, Sathyamangalam

IFSC Code : UTIB0000368

Registration link:

https://forms.gle/JLjiMufw8RjHHZUT9

IMPORTANT DATE:

Last date for full paper Submission	: February 15, 2024
Notification of acceptance	: February 17, 2024
Last Date of Registration	: February 20, 2024

REGISTRATION FEE (PER PAPER)

UG students and Research Scholars	: Rs 500/-
Faculty members	: Rs 700/-
Industry Participants	: Rs 1000/-

PUBLICATION

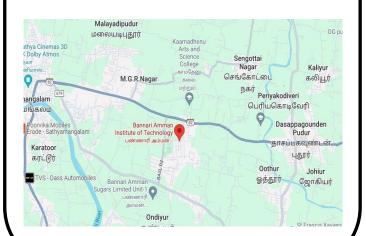
Selected papers will be published in BIT – Indian Journal of Engineering, Science, and Technology (IJEST), and Scopus-indexed journals based on the author's interest.

ADDRESS FOR COMMUNICATION

Dr CL Chinnadurrai AP/EEE
Electrical and Electronics Engineering,
Bannari Amman Institute of Technology Sathyamangalam,
Erode Dt, Tamil Nadu 638401 Phone: 04295 226191
Contact No: 9600770944,
Mail Id: epics@bitsathy.ac.in

HOW TO REACH?

Sathyamangalam is well connected by road from all major cities like Erode, Coimbatore, Mysore, Bangalore, Chennai, Kochi etc., The nearest railway stations are Erode (65 Km) and Coimbatore (70 Km).



3RD NATIONAL CONFERENCE ON ENERGY POWER INTEGRATED CIRCUITS AND SYSTEMS (EPICS-2024)

FEBRUARY 22, 2024



Organised by



Electrical and Electronics EngineeringBannari Amman Institute of Technology

(Autonomous Institution Affiliated to Anna University Chennai) Approved by AICTE Accredited by NBA New Delhi and NAAC with 'A+' Grade Sathyamangalam Erode Dt, Tamil Nadu - 638401

ABOUT THE INSTITUTE

Bannari Amman Educational Trust was established in 1995 with a vision to create educational facilities for students in rural areas. In pursuance of the vision, the Trust started Bannari Amman Institute of Technology (BIT) in 1996 at Sathyamangalam. The institution offers 19 Undergraduate programmes and 5 Postgraduate programmes in Engineering and Technology, Management, and 13 Ph.D. programmes of Engineering and Technology. BIT is an autonomous institution affiliated to Anna University, Chennai, and approved by the All India Council for Technical Education (AICTE), New Delhi, accredited by NAAC with an 'A+' Grade (CGPA of 3.36 on a 4-point scale) in the third cycle. Furthermore, 13 departments at BIT have received accreditation from the National Board of Accreditation (NBA), and 8 of the undergraduate programs are accredited by the Institution of Engineering and Technology (IET-UK).

ABOUT THE DEPARTMENT

The Department of Electrical and Electronics Engineering was established in 1996. It offers a four-year Undergraduate Programme of Bachelor of Engineering. Under the supervision of the faculty of Electrical Engineering, the department also provides a Ph.D./MS by research program. B.E Electrical and Electronics Engineering Programme is accredited by IET, United Kingdom from 2016 to 2023 and NBA, New Delhi 6 Years in Tier –I Washington accord. The department boasted a prominent staff team with 13 doctorates and 26 postgraduates, 22 of whom have registered for Ph.D. in various fields of Electrical Engineering and have rich industry, research and teaching experience. The library comprises 7535 books, 242 International / National journals and publications subscriptions related to the department.

ABOUT THE CONFERENCE

The National conference on Energy Power Integrated Circuits and Systems (EPICS) is to be held at Bannari Amman Institute of Technology, Sathyamangalam, Erode, Tamil Nadu, India. EPICS is the premier interdisciplinary platform that will deliver the latest developments and innovative research results in electrical sciences for all professionals, researchers and engineers from all over the world. This conference aims to foster interaction in the area of Energy, Power, Integrated Circuits and systems and its applications among scientists in both academia and industry. The conference includes a keynote address and invited talks by eminent speakers and presentations by participants. This conference provides engineers and academia an opportunity to share their experiences in recent development, current practices and future trends in power and energy. This event will provide a unique platform for discussing issues and developments in electrical power, power electronics, energy and its associated engineering fields. "EPICS" also provides the global platform a networking opportunity for establishing international relationships, widen professional contact and create new opportunities. Also it provides a perfect environment for the participants to up- grade their knowledge in power and energy engineering to discuss the ways to disseminate the awareness of the latest developments and advances in the field of Electrical Engineering and Technology.

CONFERENCE AREAS

- Distributed Generation and Energy Management,
- Power Electronics, Drives and Machines,
- Power System Protection, Operation & Control,
- Renewable Energy and Grid Integration,
- AI, ML, Big Data and Cyber Security,
- HV Engineering and Power System Planning,
- Economics, Electricity Markets, and Regulatory framework.
- Power Quality Mitigation Techniques

ABSTRACT AND PAPER SUBMISSION GUIDELINES

Per paper limited to 4 authors Paper Template: IEEE format

ADVISORY COMMITTEE

Dr. C Palanisamy, Principal

Dr. K Sivakumar, Dean-PDS

Dr. K L Senthilkumar, Head Academics

TECHNICAL COMMITTEE

Dr. Anand Prakash Dwivedi, IIT, Kanpur

Dr. Bhim Singh, IIT Delhi

Dr. Anil M Kulkarni, IIT Bombay

Dr. S.Thangavel, National Institute of Technology, Puducherry

Dr. Selvan M P, National Institute of Technology, Trichy

Dr Moorthi S, National Institute of Technology, Trichy

Dr. Suman M, Motilal Nehru National Institute of Technology, Allahabad

Dr Aruldoss Albert Victoire T, Anna University, Coimbatore Campus

Dr Binu Ben Jose D R, Vellore Institute of Technology, Chennai

ORGANIZING COMMITTEE

Convenor

Dr Maheswari K T Assistant Professor SI. Grade & Head Department of Electrical and Electronics Engineering

Organizing Secretary

Dr CL Chinnadurrai AP/EEE
Dr B Goldvin Sugiritha Dhas AP/EEE
Dr J Senthil Kumar AP/EEE

Coordinators

Dr M Gowrishankar AP/EEE Mrs G Nithya AP/EEE Mr N Rishikesh AP/EEE