



BANNARI AMMAN INSTITUTE OF TECHNOLOGY

(Autonomous Institution Affiliated to Anna University Chennai Approved by AICTE-Accredited by NAAC with 'A+' Grade)

Sathyamangalam – 638401, Erode (Dt), Tamilnadu

FIFTH NATIONAL CONFERENCE ON RENEWABLE AND SUSTAINABLE ENERGY TECHNOLOGIES (RESET) – 2026 (Hybrid Mode)

5 & 6 March 2026

Organized by

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

ABOUT THE INSTITUTE

Bannari Amman Educational Trust was established in 1995 with a vision to create educational facilities for students in rural areas. In pursuance of the vision, the Trust started Bannari Amman Institute of Technology (BIT) in 1996 at Sathyamangalam. The institution offers 10 Undergraduate programmes and 3 Postgraduate programmes in Engineering and Technology, Management, and 13 Ph.D. programmes of Engineering and Technology. BIT is an autonomous institution affiliated to Anna University, Chennai, and approved by the All India Council for Technical Education (AICTE), New Delhi, accredited by NAAC with an 'A+' Grade in the third cycle. The spacious and earth-connected buildings with their landscaped courtyards and pathways are designed to emphasise business ethics and the characteristics of an excellent learning centre. The campus houses well-planned academic blocks, computer centres, lecture halls, libraries, laboratories, conference rooms, staff quarters, residence halls and student centres.

ABOUT THE DEPARTMENT

The Department of Electrical and Electronics Engineering was started in the year 1996 and presently it offers undergraduate programmes in Electrical and Electronics Engineering. The undergraduate programme has been accredited for 3 years by NBA, New Delhi (Tier - 1- Washington Accord) and 8 years by Institution of Engineering and Technology (IET), U.K. The department offers Ph.D. / M.S. (By Research) in Electrical and Electronics Engineering (Full Time / Part Time). There are 4 recognized supervisors of Anna University in the department. The department boasted a prominent staff team with 15 Doctorates and 6 Post Graduates who have registered for Ph.D in various fields of Electrical Engineering and have a rich industry, research and teaching experience. The program focuses on building a strong foundation in Electrical and Electronics engineering, enabling students to develop practical skills and contribute effectively to industry and society with excellent state of art industry collaborating laboratories to facilitate the learners on world class standards.

ABOUT THE CONFERENCE

Energy is the single most valuable resource for human activity and the basis for all human progress. Rapidly increasing energy demand and growing concern about economic and environmental consequences call for a shift to clean energy and technologies. Renewable energy sources have the ability to provide energy free of air pollutants and greenhouse gasses by emitting zero or nearly zero percent of these gasses. Therefore, the International Conference on Renewable Technologies (RESET) is dedicated to advance and disseminate knowledge on methods, and technologies for increasing the sustainability of development. The primary goal of RESET is to provide a platform to promote sharing of ideas in finding alternative energy solutions to current environmental and climate issues. This conference will provide a prodigious forum for the exchange of latest technical information, dissemination of the high-quality research results, the presentation of the new developments in the area, the shaping of future directions, priorities for sustainable development and energy security. It is also providing a premier interdisciplinary platform for researchers, academia, professionals, and students to present and discuss the most recent innovations, trends, and concerns as well as practical challenges encountered and solutions adopted in the fields of Renewable and Sustainable Energy Technologies. This conference strives to offer plenty of networking opportunities and interact with leading scientists, researchers, friends and colleagues. The conference includes technical sessions, tutorials and technology and business panels. Prospective authors are invited to submit the papers to the conference. The organizing committee of the conference is pleased to invite prospective authors to submit their original manuscripts to RESET 2024. The quality papers (Plagiarism: less than 15%) will be published in SCOPUS and WoS indexed journals.

CALL FOR PAPERS

National Conference on Renewable and Sustainable Energy Technologies (RESET) is evident due to the massive deployment of future technologies. The topics of interest includes "Recent research in Sustainable Energy, Power Integrated Circuits and Systems" but are not limited to...

- Power and Energy Systems
- Industrial Control & Applications
- Power Electronics
- Drives and Machines
- Smart Electronics
- Renewable Energy and Grid Integration
- Industrial Communications and Signal Processing
- Economics, Electricity Markets and Regulatory framework
- Power Quality Mitigation Techniques

Conference Fee Details:

Registration fee (inclusive of GST)

Indian Participants	
Industrial Experts	INR 1000/-
Research Scholars & Faculty	INR 750/-
UG, PG Students	INR 600/-

*Publication with additional Article Processing Charge (APC) as per the agreement with the publishers.

Paper Submission Guidelines:

All accepted and registered abstracts will be published in the conference proceedings with ISBN registration. Further, upon acceptance of the abstract, the corresponding author may submit the full-length manuscript prepared in the given template as per the journal requirements. The selected papers will be proposed for publishing in good quality journals based on willingness of the authors. All papers submitted as per the guidelines will undergo double-blinded peer review and the acceptance will be based on the relevance, originality, contribution, validity, and presentation. Based on the recommendation from the editorial board, the camera-ready paper must be submitted for final acceptance.

Journals of Interest for Publication:

Selected high-quality papers will be considered for publication in Q3 or Q4 Scopus-indexed journals, based on the authors' preference, with additional publication charges.

Payments Details*

Mode of Payment : Online Transfer
Bank Name : Axis Bank
A/C Name : BIT-Center for Advance Studies
A/C Number : 911010036862582
Branch Name : Sathyamangalam
IFSC Code : UTIB0000368
Swift Code : AXISINBB118

Full Paper Submission:

- Paper Submission to icreset@bitsathy.ac.in
- Website Link: <https://icreset.bitsathy.ac.in>
- Registration Link: <https://forms.gle/1JEa9ucSq6GfJhcs8>

Important Dates:

• Paper Submission Deadline	09.02.2026
• Notification of Acceptance	13.02.2026
• Camera Ready Paper Submission	24.02.2026
• Registration Deadline	24.02.2026

Contact Us:

Dr S SATHISHKUMAR,
Associate Professor, Department of EEE
+91 99421 30500 | sathishkumars@bitsathy.ac.in
Dr C L CHINNADURAI,
Assistant Professor Level III, Department of EEE
+91 96007 70944 | chinnadurail@bitsathy.ac.in
Dr P SRITHA,
Assistant Professor Level III, Department of EEE
+91 99442 94545 | srihapat@bitsathy.ac.in



for Registration

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